9. The font memory according to claim 2, wherein an exclusive address is given to each dot forming the dot pattern, and the font data is information representing the dot pattern using the address exclusive to a particular dot.

10. The font memory according to claim 3, wherein an exclusive address is given to each dot forming the dot pattern, and the font data is information representing the dot pattern using the address exclusive to a particular dot.

11. The font memory according to claim 8, wherein an exclusive address is given to each dot forming the dot pattern, and the font data is information representing the dot pattern using the address exclusive to a particular dot.

12. The font memory according to claim 2, wherein the dot pattern is divided by a first division unit into a plurality of pattern areas, an address for identifying the relevant pattern area is allocated to each of the created pattern areas, each pattern area divided by the first division unit is further divided by a second division unit into a plurality of pattern areas, and an address for identifying the relevant pattern area is allocated to each of the pattern areas created using the second division unit, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

13. The font memory according to claim 3, wherein the dot pattern is divided by a first division unit into a plurality of pattern areas, an address for identifying the relevant pattern area is allocated to each of the created pattern areas, each pattern area divided by the first division unit is further divided by a second division unit into a plurality of pattern areas, and an address for identifying the relevant pattern area is allocated to each of the pattern areas created using the second division unit, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

14. The font memory according to claim 8, wherein the dot pattern is divided by a first division unit into a plurality of pattern areas, an address for identifying the relevant pattern area is allocated to each of the created pattern areas, each pattern area divided by the first division unit is further divided by a second division unit into a plurality of pattern areas, and an address for identifying the relevant pattern area is allocated to each of the pattern areas created using the

second division unit, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

15. The font memory according to claim 9, wherein the dot pattern is divided by a first division unit into a plurality of pattern areas, an address for identifying the relevant pattern area is allocated to each of the created pattern areas, each pattern area divided by the first division unit is further divided by a second division unit into a plurality of pattern areas, and an address for identifying the relevant pattern area is allocated to each of the pattern areas created using the second division unit, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

16. The font memory according to claim 10, wherein the dot pattern is divided by a first division unit into a plurality of pattern areas, an address for identifying the relevant pattern area is allocated to each of the created pattern areas, each pattern area divided by the first division unit is further divided by a second division unit into a plurality of pattern areas, and an address for identifying the relevant pattern area is allocated to each of the pattern areas created using the second division unit, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

17. The font memory according to claim 11, wherein the dot pattern is divided by a first division unit into a plurality of pattern areas, an address for identifying the relevant pattern area is allocated to each of the created pattern areas, each pattern area divided by the first division unit is further divided by a second division unit into a plurality of pattern areas, and an address for identifying the relevant pattern area is allocated to each of the pattern areas created using the second division unit, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

18. The font memory according to claim 2, wherein the dot pattern is divided into quarter pattern areas, two bit addresses 00, 01, 10, and 11 are allocated to each of the created pattern areas, each created pattern area is further divided into quarter pattern areas, and two bit addresses

00, 01, 10, and 11 are further allocated to each of the created pattern areas, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

19. The font memory according to claim 3, wherein the dot pattern is divided into quarter pattern areas, two bit addresses 00, 01, 10, and 11 are allocated to each of the created pattern areas, each created pattern area is further divided into quarter pattern areas, and two bit addresses 00, 01, 10, and 11 are further allocated to each of the created pattern areas, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

20. The font memory according to claim 8, wherein the dot pattern is divided into quarter pattern areas, two bit addresses 00, 01, 10, and 11 are allocated to each of the created pattern areas, each created pattern area is further divided into quarter pattern areas, and two bit addresses 00, 01, 10, and 11 are further allocated to each of the created pattern areas, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

21. The font memory according to claim 9, wherein the dot pattern is divided into quarter pattern areas, two bit addresses 00, 01, 10, and 11 are allocated to each of the created pattern areas, each created pattern area is further divided into quarter pattern areas, and two bit addresses 00, 01, 10, and 11 are further allocated to each of the created pattern areas, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

22. The font memory according to claim 10, wherein the dot pattern is divided into quarter pattern areas, two bit addresses 00, 01, 10, and 11 are allocated to each of the created pattern areas, each created pattern area is further divided into quarter pattern areas, and two bit addresses 00, 01, 10, and 11 are further allocated to each of the created pattern areas, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

23. The font memory according to claim 11, wherein the dot pattern is divided into quarter pattern areas, two bit addresses 00, 01, 10, and 11 are allocated to each of the created pattern areas, each created pattern area is further divided into quarter pattern areas, and two bit

addresses 00, 01, 10, and 11 are further allocated to each of the created pattern areas, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

24. The font memory according to claim 12, wherein the dot pattern is divided into quarter pattern areas, two bit addresses 00, 01, 10, and 11 are allocated to each of the created pattern areas, each created pattern area is further divided into quarter pattern areas, and two bit addresses 00, 01, 10, and 11 are further allocated to each of the created pattern areas, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

25. The font memory according to claim 13, wherein the dot pattern is divided into quarter pattern areas, two bit addresses 00, 01, 10, and 11 are allocated to each of the created pattern areas, each created pattern area is further divided into quarter pattern areas, and two bit addresses 00, 01, 10, and 11 are further allocated to each of the created pattern areas, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

26. The font memory according to claim 14, wherein the dot pattern is divided into quarter pattern areas, two bit addresses 00, 01, 10, and 11 are allocated to each of the created pattern areas, each created pattern area is further divided into quarter pattern areas, and two bit addresses 00, 01, 10, and 11 are further allocated to each of the created pattern areas, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

27. The font memory according to claim 15, wherein the dot pattern is divided into quarter pattern areas, two bit addresses 00, 01, 10, and 11 are allocated to each of the created pattern areas, each created pattern area is further divided into quarter pattern areas, and two bit addresses 00, 01, 10, and 11 are further allocated to each of the created pattern areas, and wherein the font data is information representing the dot pattern using the addresses obtained by repeating the above division and address allocation thereafter for an optional number of times.

28. The font memory according to claim 16, wherein the dot pattern is divided into quarter pattern areas, two bit addresses 00, 01, 10, and 11 are allocated to each of the created pattern areas, each created pattern area is further divided into quarter pattern areas, and two bit